Docket No.

216692US2

IN RE APPLICATION OF: Takashi YAMADA, et al.

SERIAL NO: 09/995,594

FILED:

November 29, 2001

FOR:

SEMICONDUCTOR CHIP WHICH COMBINES BULK AND SOI REGIONS AND SEPARATES SAME WITH

PLURAL ISLOLATION REGIONS

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

## SIR:

Transmitted herewith is an amendment in the above-identified application.

- No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE			CALCULATIONS	
TOTAL	16	MINUS	31	0	х	\$18	=	\$0.00	
INDEPENDENT	4	MINUS	9	0	х	\$86	=	\$0.00	
		☐ MULTIPLE DEPENDENT CLAIMS + \$290				=	\$0.00		
		TOTAL OF ABOVE CALCULATIONS					\$0.00		
		☐ Reduction by 50% for filing by Small Entity					\$0.00		
		☐ Recordation of Assignment				\$40	=	\$0.00	
		TOTAL					\$0.00		

Ш	Αc	heck	in	the	amount	of	*\$0.00 i	s attac	:hed	•
---	----	------	----	-----	--------	----	-----------	---------	------	---

- ☐ Credit card payment form is attached to cover the fees in the amount of \$0.00
- Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Registration No. 28,870

Customer Number

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

Katherine D. Pauley Registration No. 50,607 Imose

DOCKET NO: 216692US2

## IN THE UNITED STATES PATENT & TRADEMARK OFFICE

RE APPLICATION OF

TAKASHI YAMADA, ET AL. : EXAMINER: DICKEY, T. L.

SERIAL NO: 09/995,594

RCE FILED: MARCH 2, 2004 : GROUP ART UNIT: 2826

FOR: SEMICONDUCTOR CHIP WHICH COMBINES BULK AND SOI REGIONS AND SEPARATES SAME WITH PLURAL

ISLOLATION REGIONS

## **SUPPLEMENTAL AMENDMENT**

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Further to the supplemental amendment filed March 12, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.